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low-voltage, low-power, and high speed design [141, [151. In this section, we summarize some basic equations for V , and S highlighting the role of the back-gate thickness x_b , for later use (Fig. 1).

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Jun 03, 2010 · Abstract. The power MOSFET structures discussed in the last two chapters utilize two-dimensional charge coupling for supporting the drain voltage when operating in the blocking mode. The charge coupling is achieved in these devices by utilizing a source electrode located within an oxide coated trench oriented orthogonal to the wafer surface.

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